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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number	10/665,999
Filing Date	Sept. 17, 2003
First Inventor	John R. Allen
Confirmation No.	8962
Group Art Unit	2124
Examiner Name	Chuong D. Ngo

Total Number Of Pages In This Submission	4+(3 ref)	Attorney Docket No.	CAT004 US

ENCLOSURES (check all that apply)

- [ ] Fee Transmittal Form (1 page in duplicate)
  - [ ] Fee Attached -- Credit Card Payment Form (1 pg)
- [ ] Amendment / Reply
  - [ ] After Final
  - [ ] Affidavits/declarations
- [ ] Extension of Time Request
- [ ] Express Abandonment Request
- [x] Information Disclosure Statement (2 pages)
- [ ] Certified Copy of Priority Document(s)
- [ ] This is a Response to Missing Parts/ Incomplete Application under 37 CFR 1.52 or 1.53
  - [ ] Copy of Notice To File Missing Parts (2 pages)

- [ ] Assignment Papers *(for an Application)*
- [ ] Drawing(s)
- [ ] Licensing-related Papers
- [ ] Petition
- [ ] Petition to Convert to a Provisional Application
- [ ] Power of Attorney, Revocation of Previous Powers; And Statement Under 37 CFR 3.73(b)
- [ ] Terminal Disclaimer
- [ ] Request for Refund
- [ ] CD, Number of CD(s)_____

- [ ] After Allowance Communication to Group
- [ ] Appeal Communication to Board of Appeals and Interferences
- [ ] Appeal Communication to Group *(Appeal Notice, Brief, Reply Brief)*
- [ ] Proprietary Information
- [ ] Status Letter
- [x] Other Enclosure(s) *(please identify below):*

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Remarks

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SIGNATURE OF APPLICANT, ATTORNEY OR AGENT

Firm or Individual Name	Omkar K. Suryadevara (Reg. No. 36,320)
	Silicon Valley Patent Group LLP
	2350 Mission College Boulevard, Suite 360
	Santa Clara, California 95054
Signature	*S. Omkar*
Date	January 14, 2005

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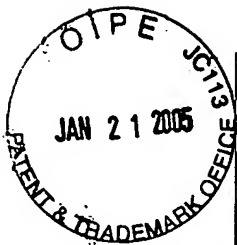
<u>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</u>

Inventors:	John R. Allen;
Assignee:	Catalytic, Inc.
Title:	Emulation Of A Fixed Point Operation Using A Corresponding Floating Point Operation

Serial No.:	10/665,999	Filing Date:	Sept. 17, 2003
Examiner:	Chuong D. Ngo	Group Art Unit:	2124
Docket No.:	CAT004 US	Confirmation No:	8962

Santa Clara, California
January 14, 2005

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, §1.97 and §1.98, the Applicants submit for consideration in the above-identified patent application the document listed on the accompanying Form PTO-1449. Copies of references numbered 4-6 are submitted herewith. The Examiner is requested to make these documents of record. The remaining references are not attached hereto, because these references are issued patents or publications which are readily available in the U.S. Patent and Trademark Office.

This Information Disclosure Statement is submitted pursuant to 37 CFR §1.97(b) as it within three months of the filing date of a national application other than a continued prosecution application and/or before the mailing of a first Office Action on the merits. Accordingly, no fee is required.

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

The information contained in this Information Disclosure Statement is to the best of my knowledge and is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

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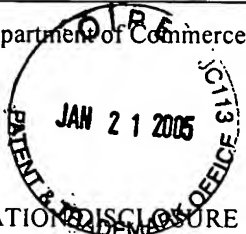
S. Omkar

Omkar K. Suryadevara

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Respectfully submitted,

S. Omkar

Omkar K. Suryadevara
Attorney for Applicants
Reg. No. 36,320

U.S. Department of Commerce, Patent and Trademark Office	Application No.:	10/665,999
	Filing Date:	Sept. 17, 2003
	First Named Inventor:	John R. Allen
	Group Art Unit:	2124
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Examiner Name:	Unknown
(Use several sheets if necessary)	Confirmation No.:	8962
	Attorney Docket No.:	CAT004 US

U.S. Patent Documents

*Examiner Intials		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1.	6,460,177	10/01/02	Lee	717	146	
	2.	6,173,247	01/09/01	Maurudis et al.	703	23	
	3.	5,222,037	06/22/93	Taniquchi	364	748	

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

	4.	P. Banerjee, "An Overview of the AccelFPGA Compiler for Mapping MATLAB Programs onto FPGAs", Prith Banerjee – UC Berkeley Talk, Nov. 6, 2002, pp. 1-33
	5.	M. Groden, ' Automatic RTL Conversion of DSP Algorithms for a Channelized Wideband Receiver", Int'l Signal Processing Conference, April 2003, Dallas, TX, pp. 1-5
	6.	"Filter Design Toolbox" For Use with MATLAB, User's Guide, Version 2, Chapter 5 "Quantization and Quantized Filtering" pp. 5.2-5.26, Chapter 6, "Working with Objects" pp. 6.1-6.10, Chapter 7, "Quantizers and Unit Quantizers, pp. 7.2-7.10, Chapter 8, "Working with Quantized Filters", pp. 8.1-8.16, Chapter 9, "Working with Quantized FFTs", pp. 9.1-9.10, Chapter 10, "Quantized Filtering Analysis Examples", pp 10.1-10.26, June, 2001

Examiner:	Date Considered:

* Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication with applicant.